

UNIVERSITY OF KERALA

**TIMETABLE FOR FIFTH SEMESTER B.TECH DEGREE EXAMINATION, MAY 2024
REGULAR – 2021 ADMISSION & SUPPLEMENTARY -2020 ADMISSION (2020 SCHEME)
UNIVERSITY COLLEGE OF ENGINEERING, KARIAVATTOM**

DAY	DATE	TIME	SUBJECT
WEDNESDAY	08.05.2024	09:30 AM-12:30 PM	ECT 301: LINEAR INTEGRATED CIRCUITS CST 301: FORMAL LANGUAGES AND AUTOMATA THEORY ITT301:WEB APPLICATION DEVELOPMENT
FRIDAY	10.05.2024	09:30 AM-12:30 PM	ECT 303: DIGITAL SIGNAL PROCESSING CST 303: COMPUTER NETWORKS ITT303: OPERATING SYSTEM CONCEPTS
MONDAY	13.05.2024	09:30 AM-12:30 PM	ECT 305: ANALOG AND DIGITAL COMMUNICATION CST 305: SYSTEM SOFTWARE ITT305: DATA COMMUNICATION AND NETWORKING
WEDNESDAY	15.05.2024	09:30 AM-12:30 PM	ECT 307: CONTROL SYSTEMS CST 307: MICROPROCESSORS AND MICROCONTROLLERS ITT 307: FORMAL LANGUAGES AND AUTOMATA THEORY
FRIDAY	17.05.2024	09:30 AM-12:30 PM	1/2 HUT 300: INDUSTRIAL ECONOMICS AND FOREIGN TRADE HUT 310: MANAGEMENT FOR ENGINEERS
			CST 309: MANAGEMENT OF SOFTWARE SYSTEMS ITT 309: MANAGEMENT FOR SOFTWARE ENGINEERS
MONDAY	27.05.2024	09:30 AM-03:30 PM	MCN 301: DISASTER MANAGEMENT (TRF)
WEDNESDAY	29.05.2024	09:30 AM-03:30 PM	ECL 331: ANALOG INTEGRATED CIRCUITS AND SIMULATION LAB CSL 331: SYSTEM SOFTWARE AND MICROPROCESSORS LAB ITL 331: OPERATING SYSTEM AND NETWORK PROGRAMMING LAB
FRIDAY	31.05.2024	09:30 AM-03:30 PM	ECL 333: DIGITAL SIGNAL PROCESSING LAB CSL 333:DATABASE MANAGEMENT SYSTEMS LAB ITL 333: WEB APPLICATION DEVELOPMENT LAB

